

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	reconf\$6 with test with system same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:53
L2	0	(reconf\$6 with test with system) same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:53
L3	9	(reconf\$6 with system) same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L4	474	(program\$5 with block with diagram) same FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L5	773	reconfig\$ with test same system	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L6	17	I4 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:55



reconfigure test system transceiver front end

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Smart Dust Mote Core Architecture - group of 7 »

B Warneke, S Bhavé - CS252, Spring2000: Project Report. Berkeley Sensor and ... - www-bsac.eecs.berkeley.edu

... In typical low power mixed-signal **systems**, most designers ... an immediate action and

those that **reconfigure** the mote ... of some of the blocks to **test** the feasibility ...

Cited by 1 - View as HTML - Web Search

A 2.4-GHz Dual-Mode 0.18-

TB Cho, D Kang, CH Heng, BS Song - IEEE JOURNAL OF SOLID-STATE CIRCUITS, 2004 - ieeexplore.ieee.org

... and therefore it is easier to **reconfigure** the **system** ... when the **transceiver** is integrated

with another **system** running at a ... 19(b). To **test** the image rejection in ...

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